

TITLE: A METHOD FOR TRANSLATING INSTRUCTIONS IN A SPECULATIVE MICROPROCESSOR FEATURING COMMITTING STATE

Inventor (s): Linus Torvalds, Robert Bedicheck, and Stephen Johnson

USSN: 09/417,980

Attorney Docket #: TRAN-P012

1/4

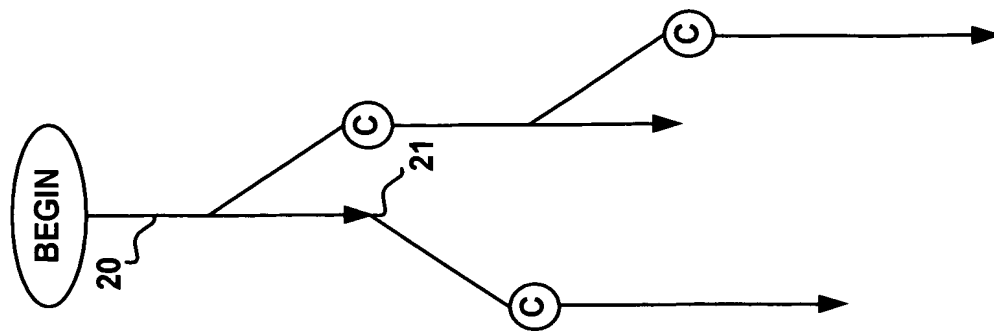


FIGURE 2

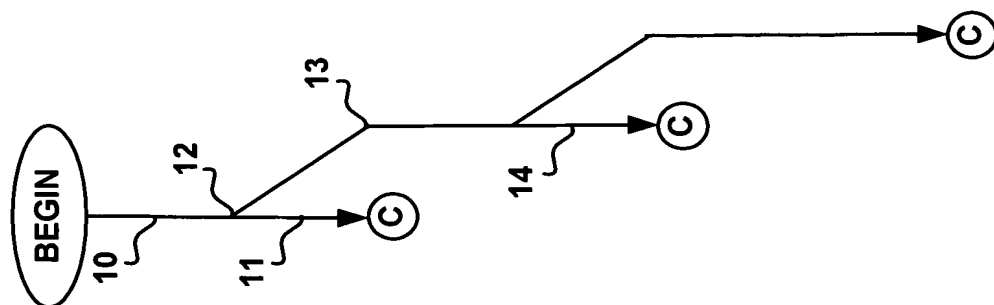
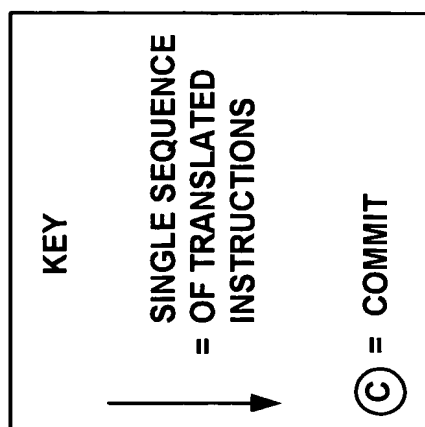


FIGURE 1



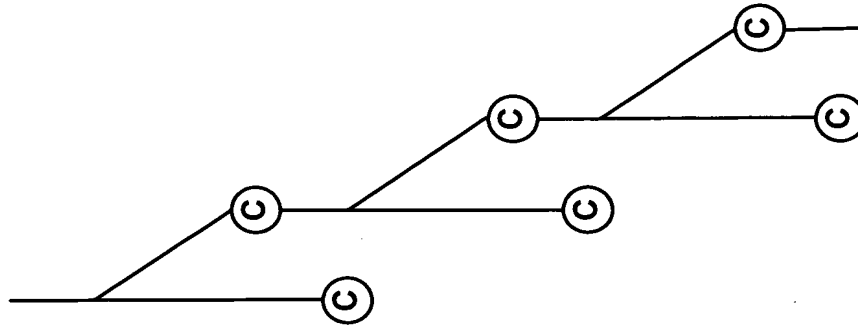
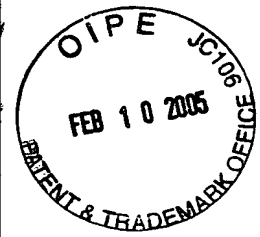


FIGURE 4

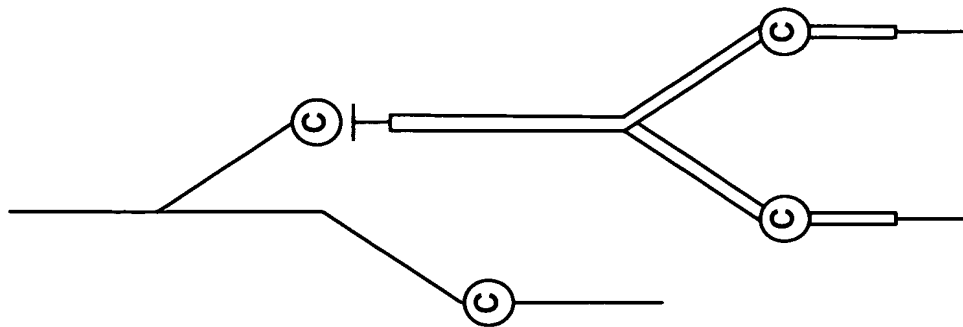
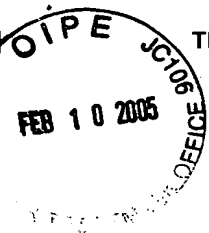
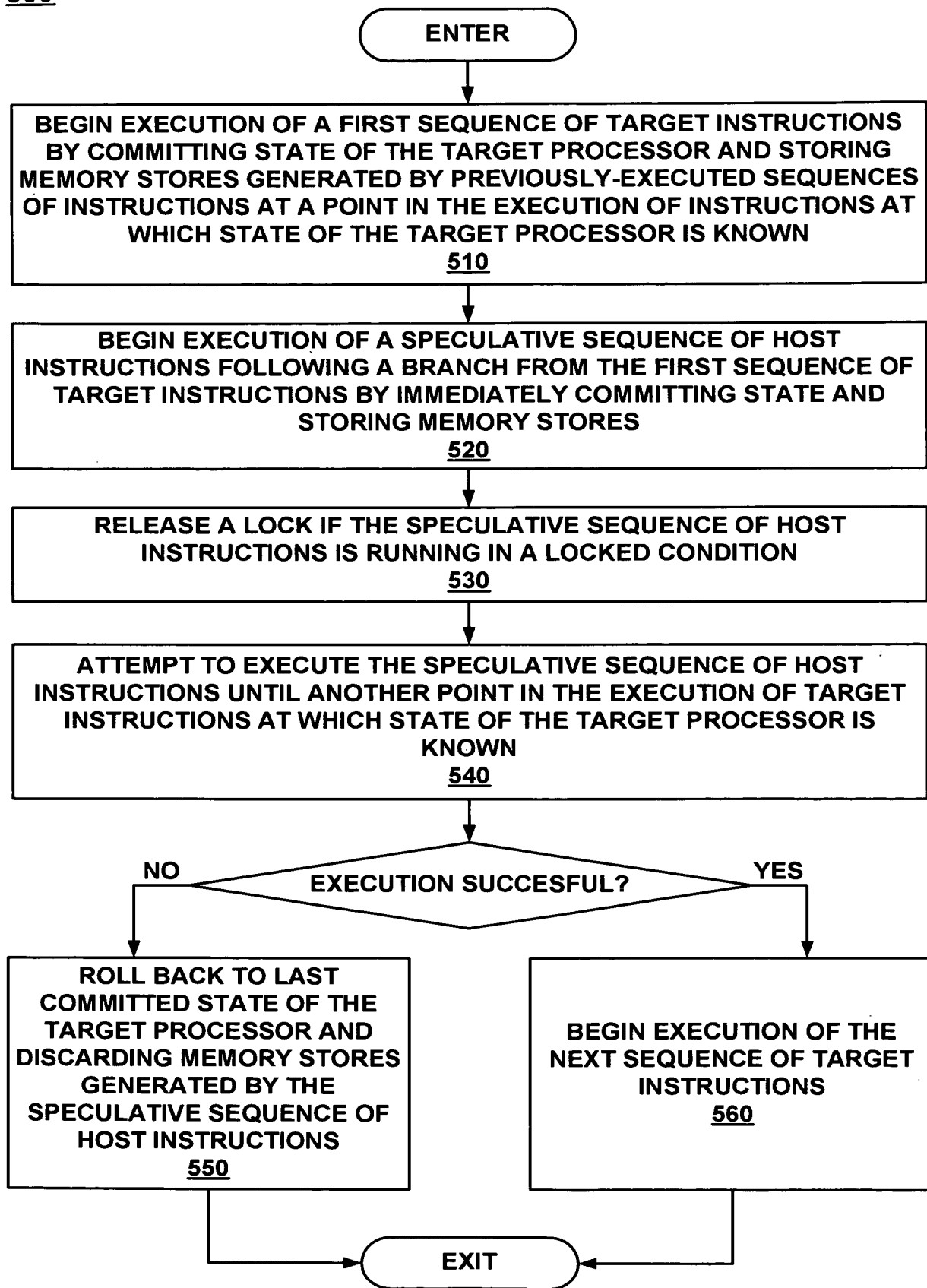


FIGURE 3



3/4

500

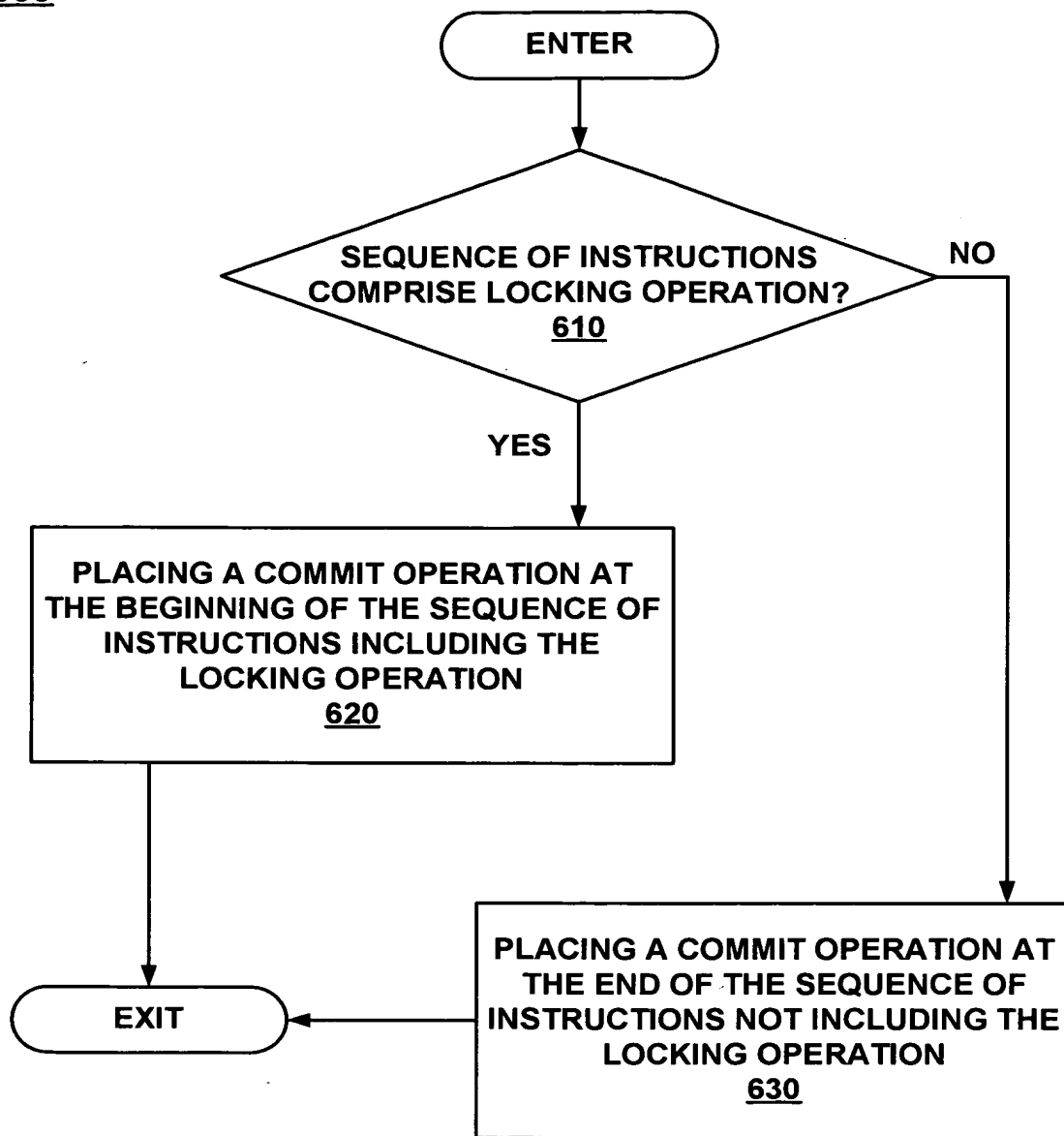


**FIGURE 5**



4/4

600



**FIGURE 6**